

SURFACE MOUNT GLASS PASSIVATED RECTIFIER

S1A THRU S1M

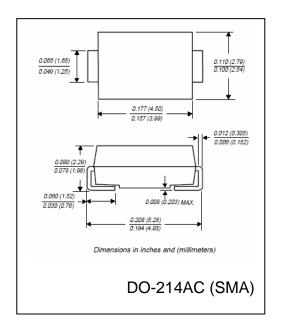
VOLTAGE RANGE CURRENT 50 to 1000 Volts 1.0 Ampere

FEATURES

- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- Built-in strain relief, ideal for automated placement
- Glass passivated chip junction
- High Temperature Soldering: 260 °C / 10 seconds, 265 °C / 5 seconds at terminals

MECHANICAL DATA

- Case: JEDEC DO-214AC molded plastic over glass passivated chip
- Terminals: Solder plated, solderable per
- MIL-STD 750, Method 2026
- Polarity: Color band denotes cathode end
- Weight: 0.002 ounce, 0.064 gram



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

- Ratings at 25°C ambient temperature unless otherwise specified
- Single Phase, half wave, 60Hz, resistive or inductive load
- For capacitive load derate current by 20%

	SYMBOLS	S1A	S1B	S1D	S1G	S1J	S1K	S1M	UNIT	
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	Volts	
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	Volts	
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	Volts	
Maximum Average Forward Rectified Current, At $T_C = 105^{\circ}C$	I _(AV)	1.0							Amps	
Peak Forward Surge Current										
8.3mS single half sine wave superimposed on	I_{FSM}	40 30						30	Amps	
rated load (JEDEC method)										
Maximum Instantaneous Forward Voltage @ 1.0A	V_{F}	1.1						Volts		
Maximum DC Reverse Current at Rated $T_A = 25$ °C	T	1.0					5.0		^	
DC Blocking Voltage per element $T_A = 125$ °C	I_R	50							μA	
Maximum Reverse Recovery Time Test conditions $I_F = 0.5A$, $I_R = 1.0A$, $I_{RR} = 0.25A$	t _{rr}	1.8							μS	
Typical Junction Capacitance (Measured at 1.0MHz and applied reverse voltage of 4.0V)	C_{J}	12							pF	
Typical Thermal Resistance (Note 1)	$R_{\theta JA}$	75 85						35	°C/W	
	$R_{ heta JL}$			27			3	30	C/ W	
Operating Junction Temperature	$T_{\rm J}$	(-55 to +150)							°C	
Storage Temperature Rang	T_{STG}	(-55 to +150)							^o C	

Notes:

1. Thermal resistance from junction to ambient and from junction to lead mounted on PCB with 0.2" x 0.2" (5.0 x 5.0nn) copper pad areas.



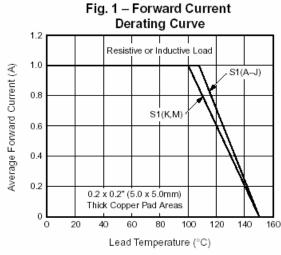


Fig. 3 - Typical Instantaneous **Forward Characteristics**

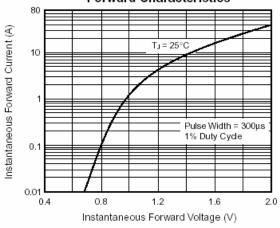


Fig. 5 – Typical Junction Capacitance

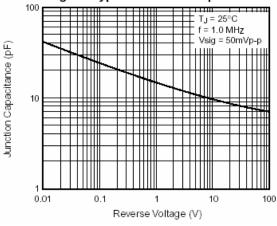


Fig. 2 – Maximum Non-Repetitive Peak Forward Surge Current

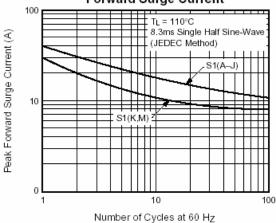
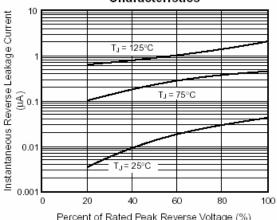


Fig. 4 - Typical Reverse Leakage Characteristics



Percent of Rated Peak Reverse Voltage (%)

