### SURFACE MOUNT HIGH EFFICIENCY RECTIFIER

# US2A THRU US2M

VOLTAGE RANGE CURRENT 50 to 1000 Volts 2.0 Ampere

#### **FEATURES**

- Plastic package has UL flammability classification 94V-0
- Glass passivated chip junction
- Built in strain relief
- Fast switching speed for high efficiency
- High temperature Soldering guaranteed: 250 °C/10 seconds

### MECHANICAL DATA

- Case: JEDEC DO-214AA transfer molded plastic
- Terminals: Solder plated, solderable per MIL-STD-750 method 2026
- Polarity: Color band denotes cathode end
- Weight: 0.003 ounce, 0.093 gram



- Ratings at 25°C ambient temperature unless otherwise specified
- Single Phase, half wave, 60Hz, resistive or inductive load
- For capacitive load derate current by 20%

0.086 (2.20) 0.077 (1.95) 0.155 (3.94) 0.130 (3.30)
0.180 (4.57) 0.160 (4.06) 0.096 (2.44) 0.084 (2.13) 0.096 (1.52) 0.090 (0.752) 0.008 (0.203) MAX. 0.220 (5.59)
Dimensions in inches and (millimeters)  DO-214AA (SMB)

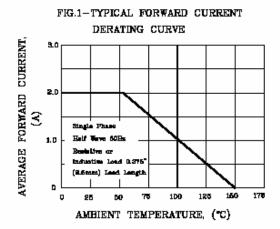
	SYMBOLS	US2A	US2B	US2D	US2G	US2J	US2K	US2M	UNIT
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage	$V_{RMS}$	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage	$V_{DC}$	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current, At $T_A = 55^{\circ}C$ (Note 1)	$I_{(AV)}$	2.0						Amps	
Peak Forward Surge Current									
8.3mS single half sine wave superimposed on	$I_{FSM}$	50						Amps	
rated load (JEDEC method)									
Maximum Instantaneous Forward Voltage @2.0A	$V_{\mathrm{F}}$		1.0		1.3		1.7		Volts
Maximum DC Reverse Current at Rated $T_A = 25$ °C	ī	5.0							μА
DC Blocking Voltage per element $T_A = 125$ °C	$I_R$	100							
Maximum Reverse Recovery Time Test conditions $I_F = 0.5A$ , $I_R = 1.0A$ , $I_{RR} = 0.25A$	$t_{rr}$	50			75			nS	
Typical Junction Capacitance (Measured at 1.0MHz and applied reverse voltage of 4.0V)	$C_{\mathrm{J}}$	50			30		pF		
Typical Thermal Resistance (Note 1)	$R_{\theta JA}$	75							<sup>O</sup> C/W
Typical Thermal resistance (Note 1)	$R_{ heta JL}$	17							
Operating Junction Temperature Range	$T_{J}$	(-55 to +150)						<sup>o</sup> C	
Storage Temperature Range	$T_{STG}$	(-55 to +150)							<sup>o</sup> C

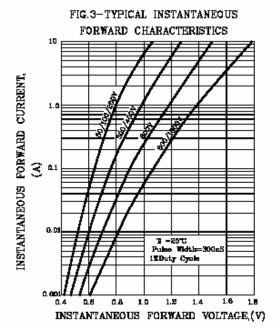
#### **Notes:**

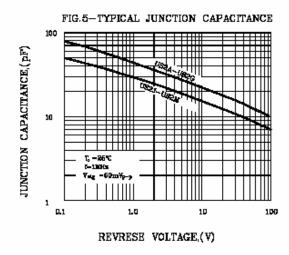
1. Thermal resistance from junction to ambient and from junction to lead mounted on PCB with 0.3" x 0.3" (8.0mm x 8.0mm) copper pad areas.

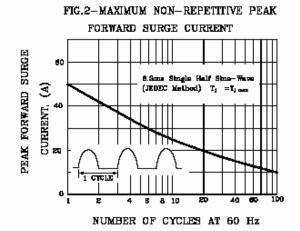


# RATINGS AND CHARACTERISTIC CURVES US2A THRUUS2M









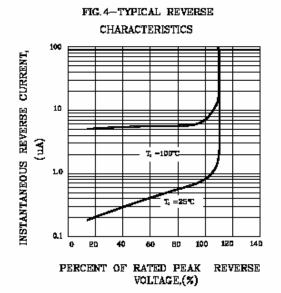
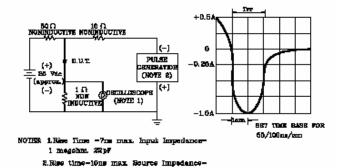


FIG.6-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



60 ohms